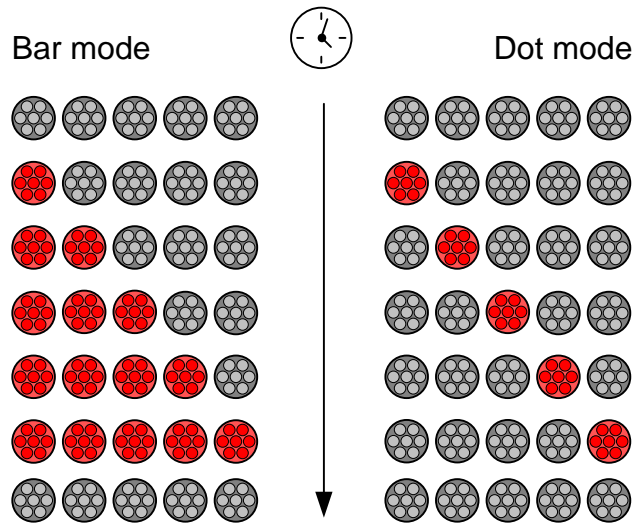
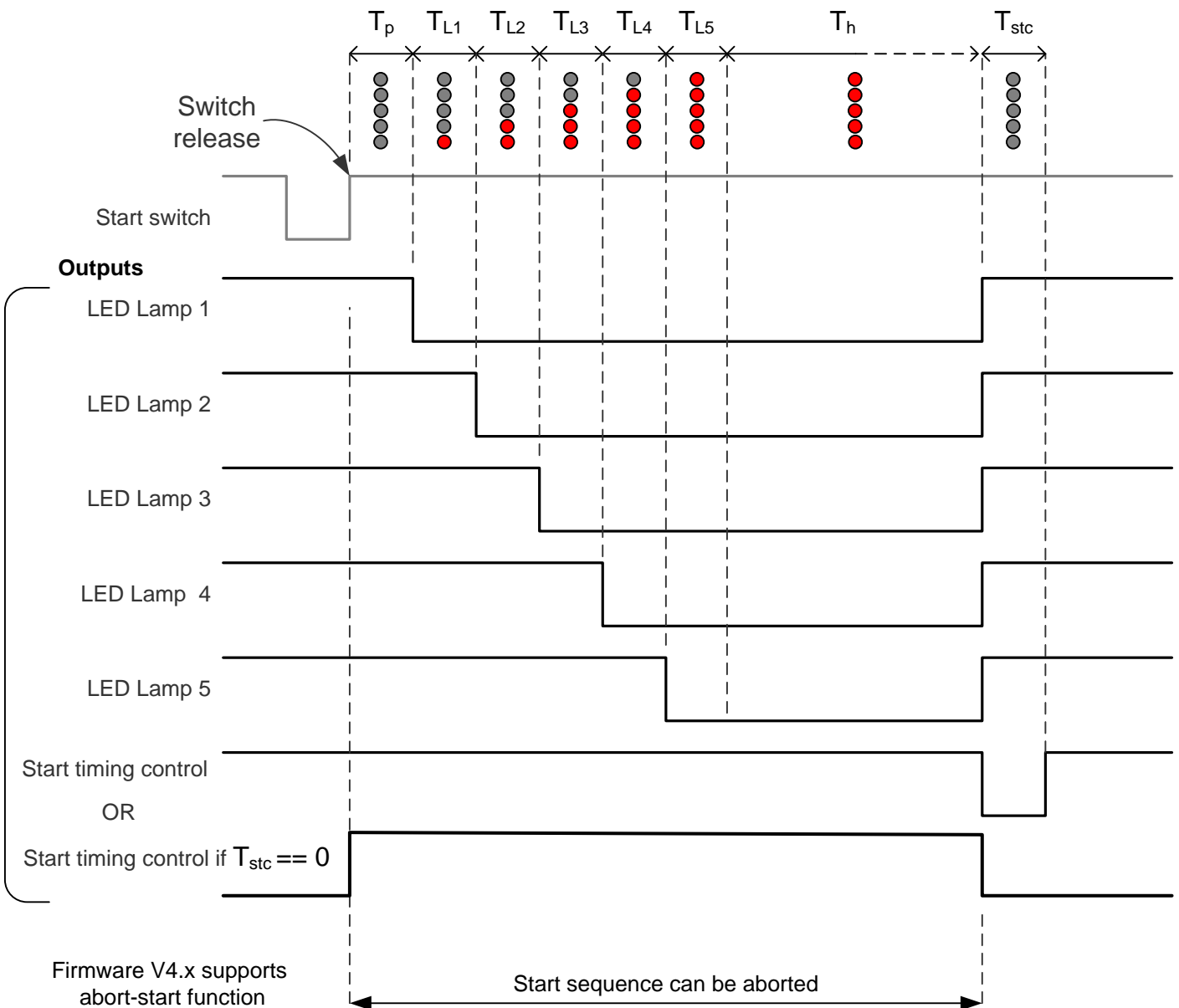


Grid Start Lights Controller

<http://picprojects.org.uk>



Timing diagram

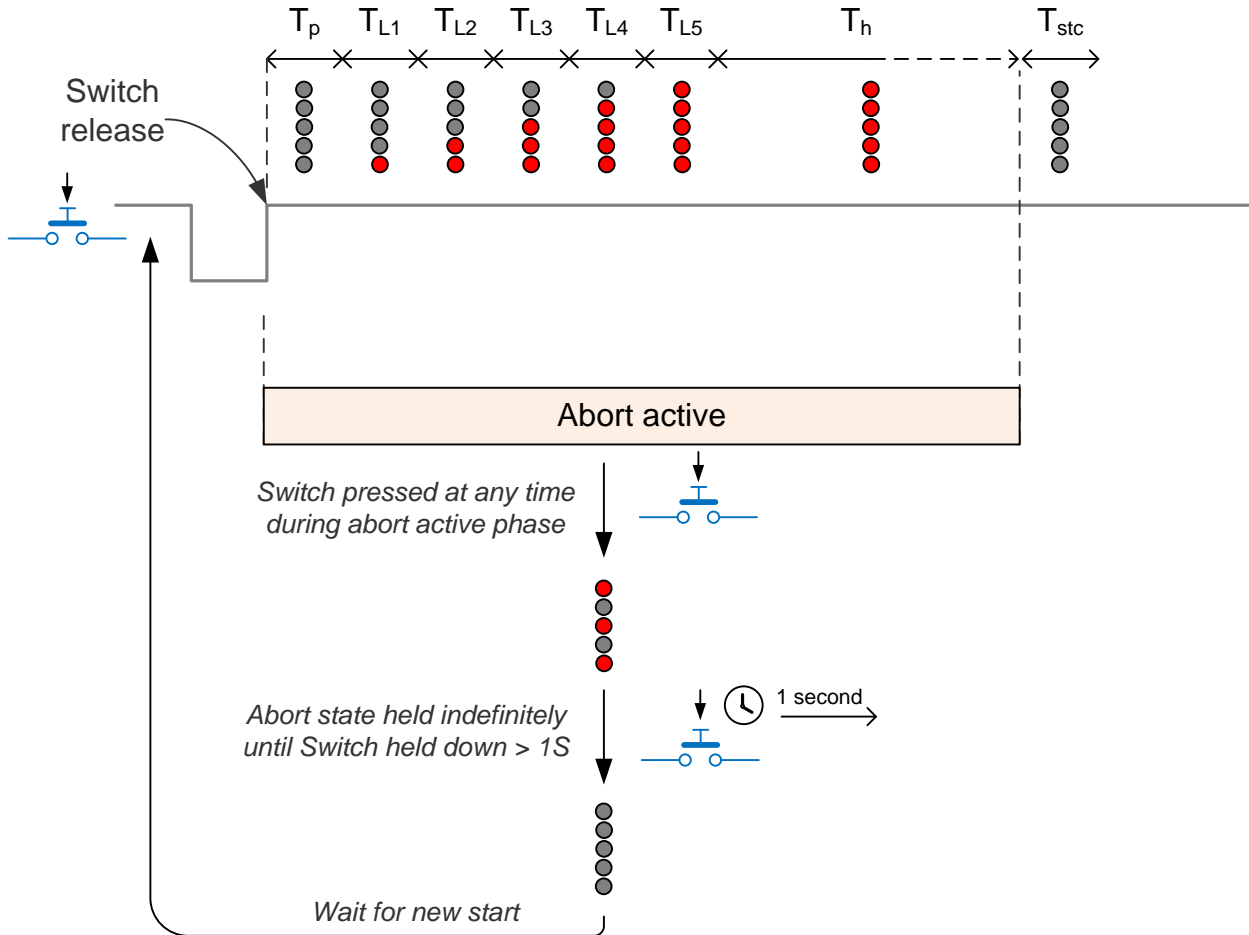


Timings shown are configurable from 0 to 25.5S in 100mS intervals
 T_h can be fixed or random delay

Grid Start Lights Controller

<http://picprojects.org.uk>

Firmware Version 4 supports an 'abort start sequence' feature. When enabled, pressing the start button a second time during the start sequence will stop the sequence and display the aborted start light pattern.



'Abort start' output light pattern

The abort light pattern is stored in the EEPROM at address 0x0A

The pattern of lights that is displayed for an 'aborted start' can be configured as shown in these examples.

A '1' in the bit pattern sets the corresponding light on.
A '0' in the bit pattern sets the corresponding light off

The three unused bits marked 'x' are ignored

